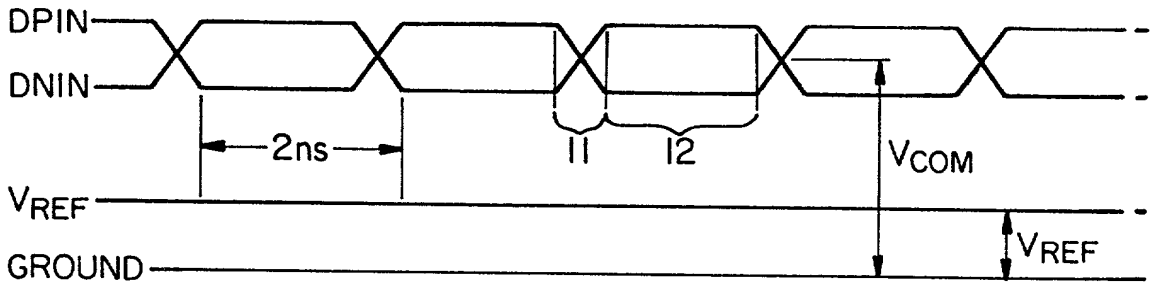
DPIN
DNIN
2ns
I1
I2
VCOM
VREF
GROUND
VREF

FIG_1

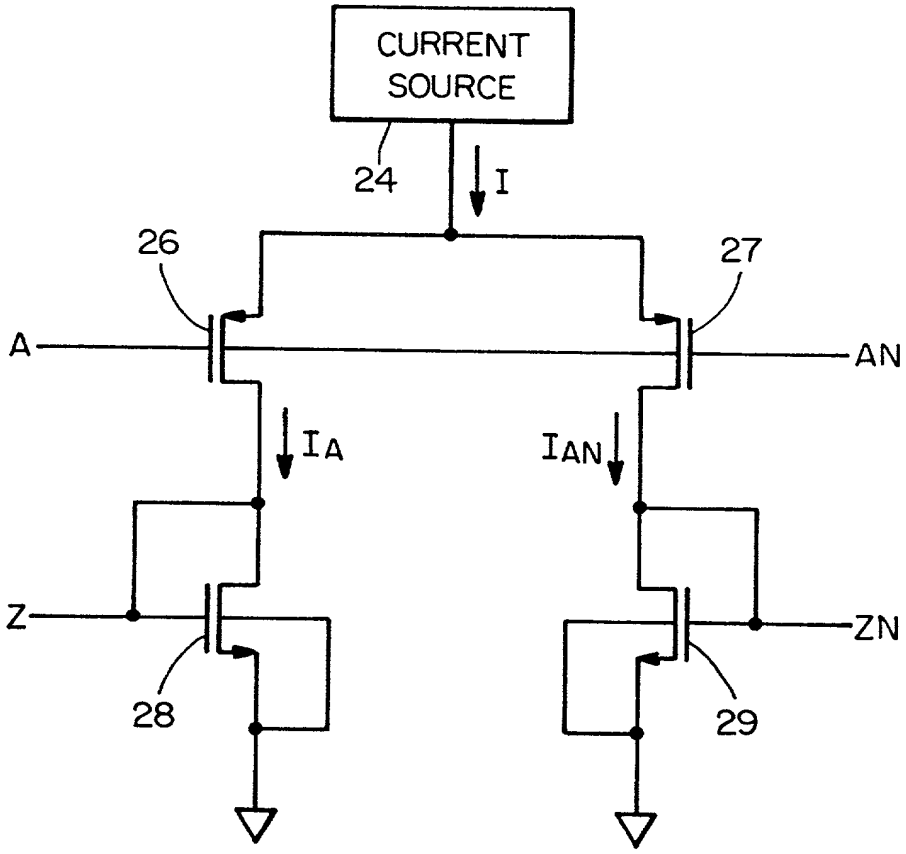
CURRENT SOURCE
24
I
26
27
A
AN
IA
IAN
Z
ZN
28
29

FIG_3

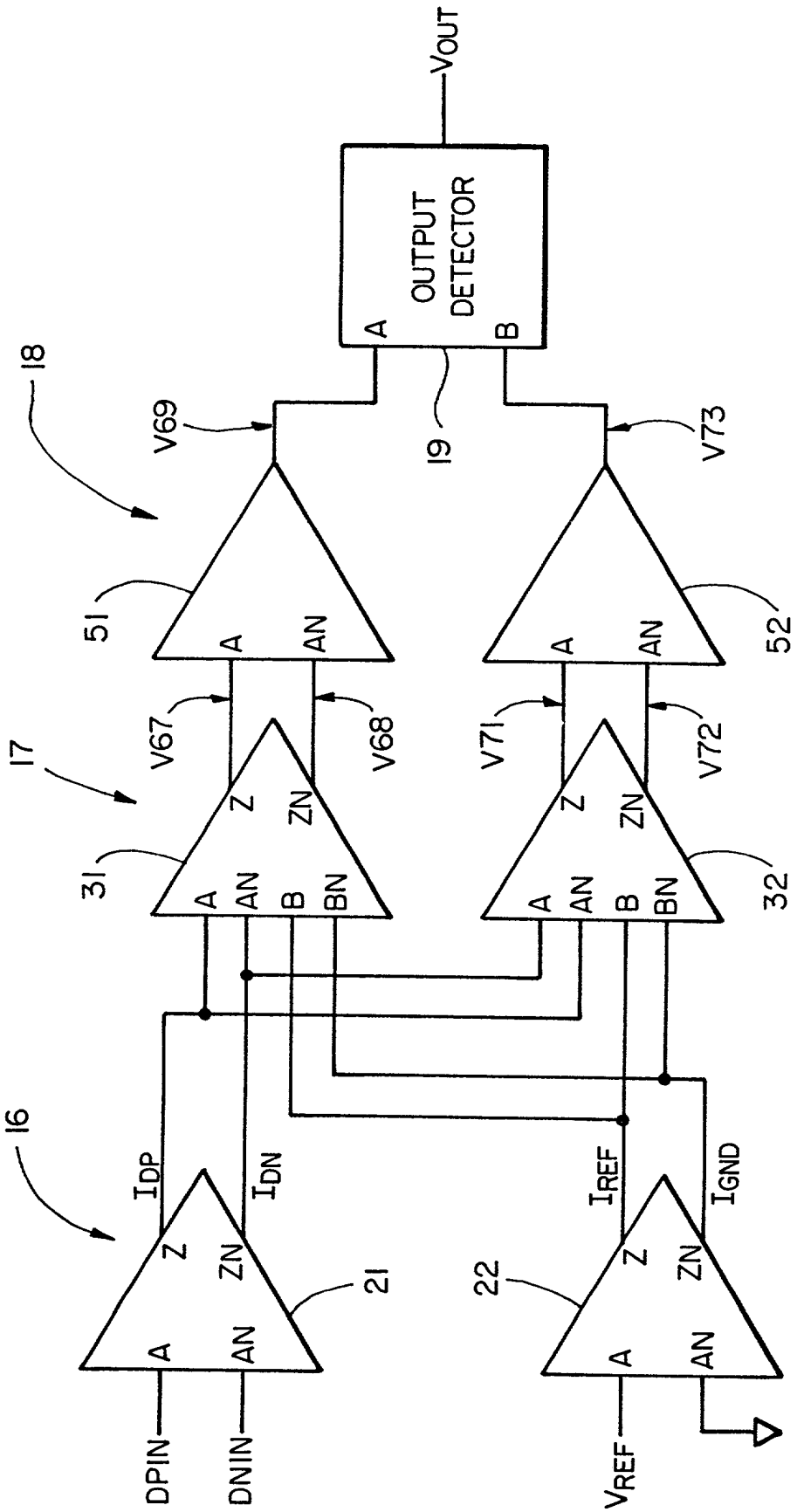
VOUT
OUTPUT DETECTOR
A
B
19
18
V69
V73
51
52
A
AN
17
V67
V68
V71
V72
Z
ZN
31
32
A
AN
B
BN
16
IDP
IDN
IREF
IGND
21
22
DPIN
DNIN
VREF

FIG_2

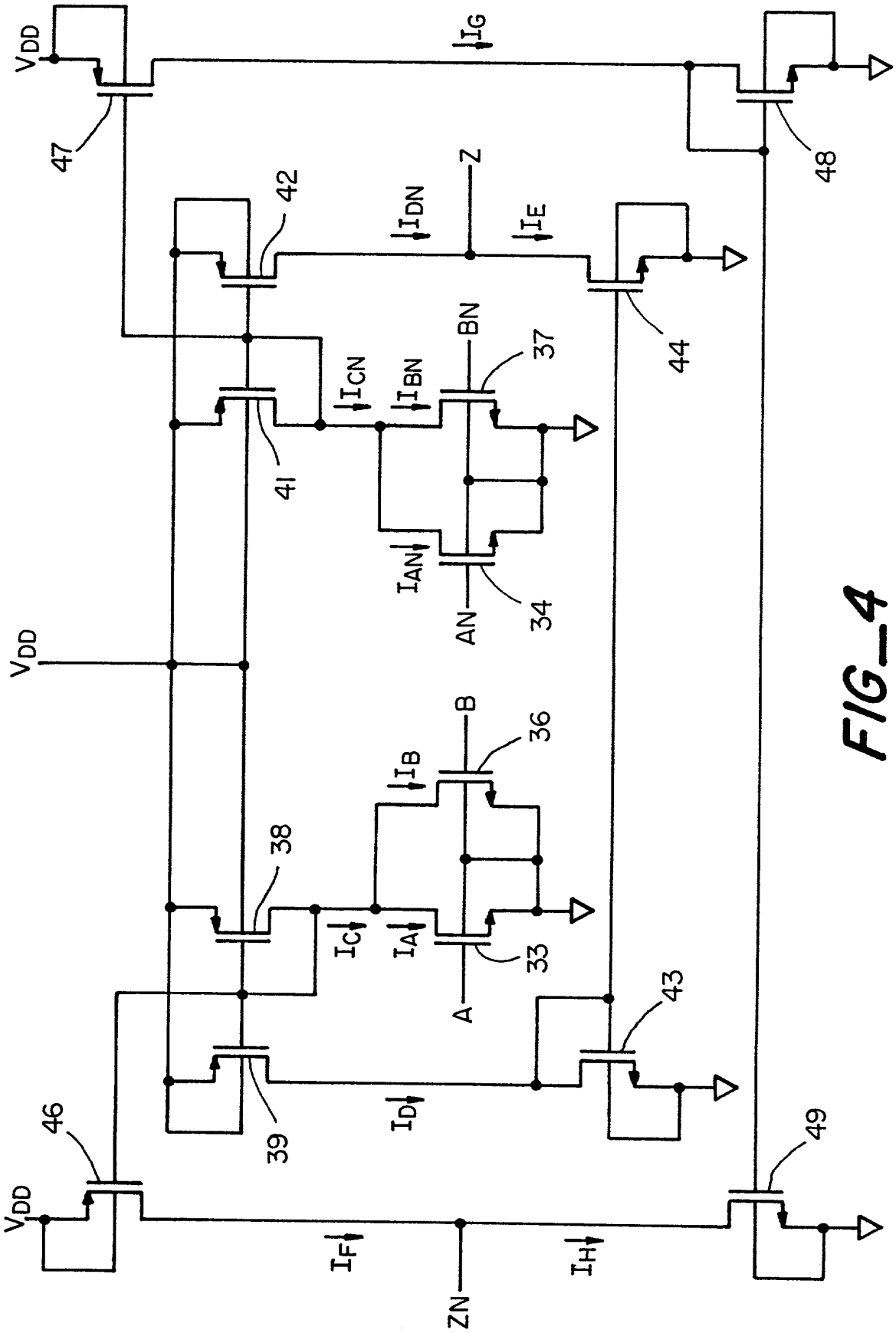
VDD
IG
47
48
42
IDN
Z
IE
44
BN
37
ICN
IBN
41
IAN
AN
34
VDD
B
36
IB
38
IC
IA
33
A
43
46
39
ID
49
VDD
IF
ZN
IH

FIG_4

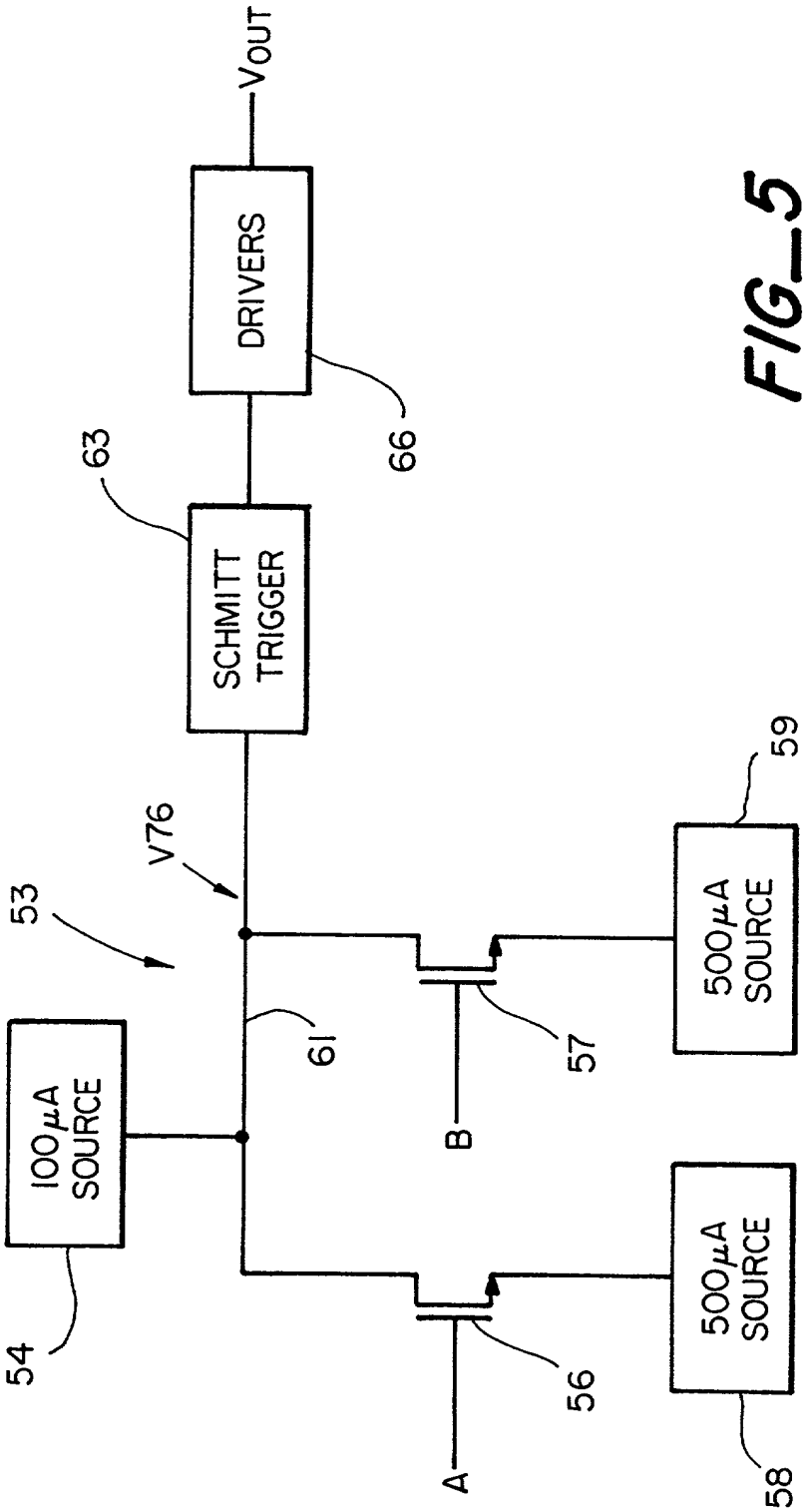
VOUT
DRIVERS
66
63
SCHMITT TRIGGER
V76
53
61
100μA SOURCE
54
500μA SOURCE
59
57
B
500μA SOURCE
58
56
A

FIG_5

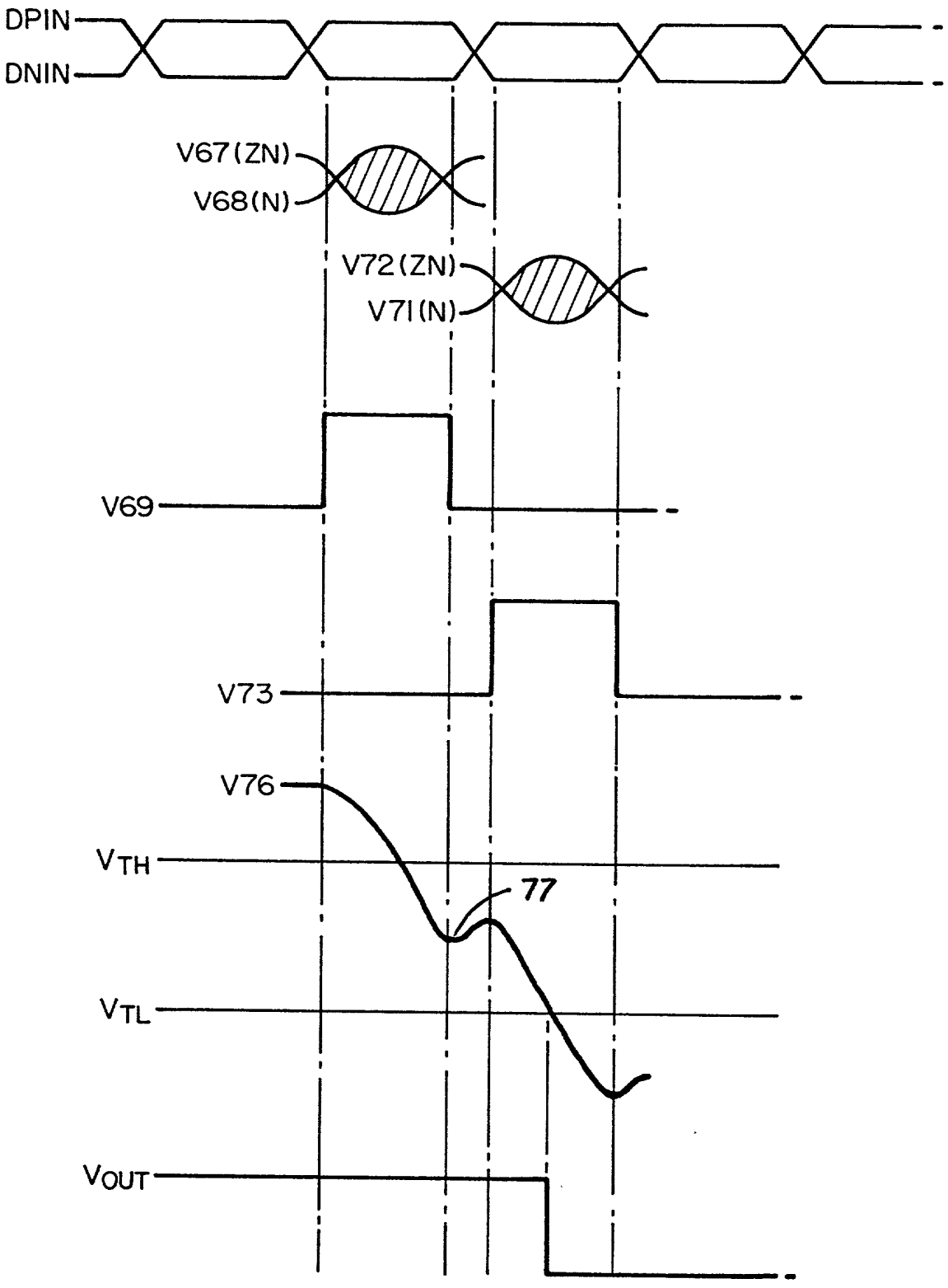

FIG_6